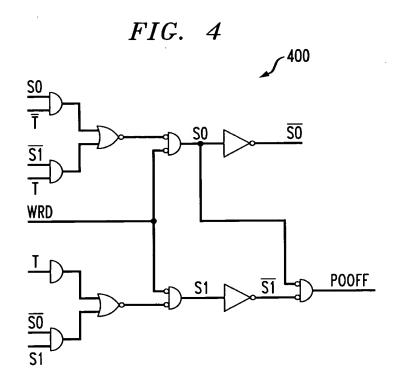


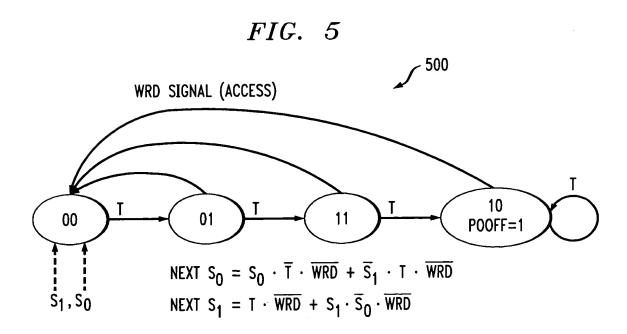
FIG. 2 PRIOR ART 120

CACHE LINE NUMBER	TAG	DATA	VALID BIT	
0				
1				
2				
		<b>:</b>	:	
C-1				
K WORDS				

FIG. 3 N-BIT GLOBAL COUNTER 310 300 TICK PULSE (T) VALID BITS CACHE ARRAY CACHE-LINE DATA/TAG ٧ CACHE-LINE DATA/TAG 2-BIT DECAY V COUNTERS CACHE-LINE DATA/TAG 320-n ٧ CACHE-LINE DATA/TAG V









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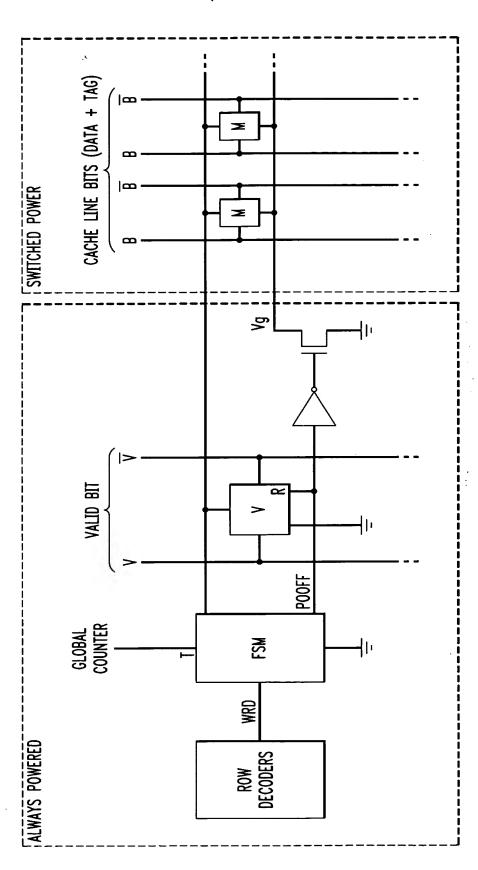


FIG. 6



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FIG. 7

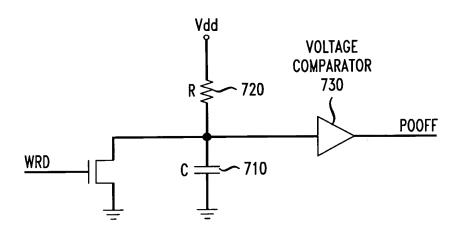


FIG. 8

800

CACHE LINE NUMBER	TAG	DATA	VALID BIT	DIRTY BIT
0				
1				
2				
:	:	<u>:</u>	:	:
C-1				

K WORDS





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FIG. 9

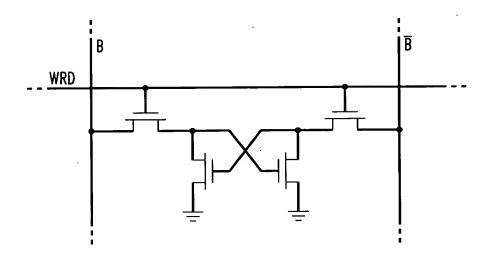


FIG. 10

